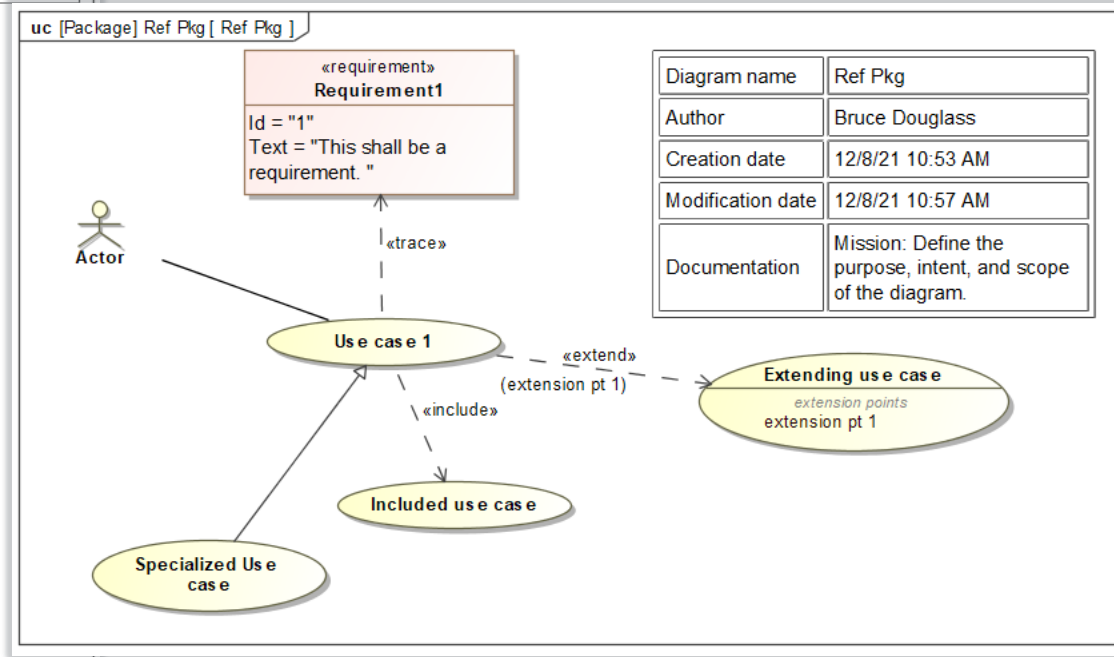
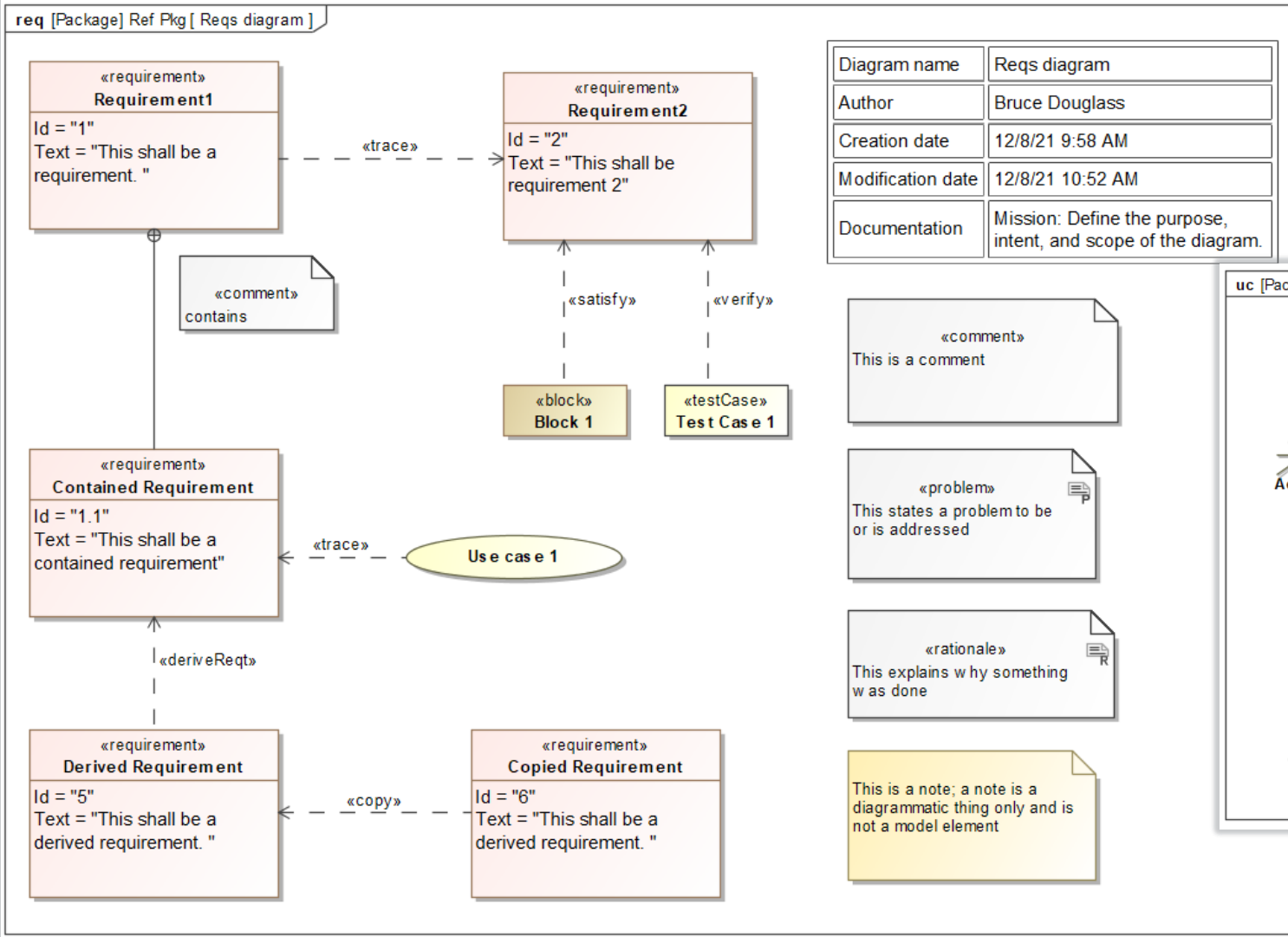


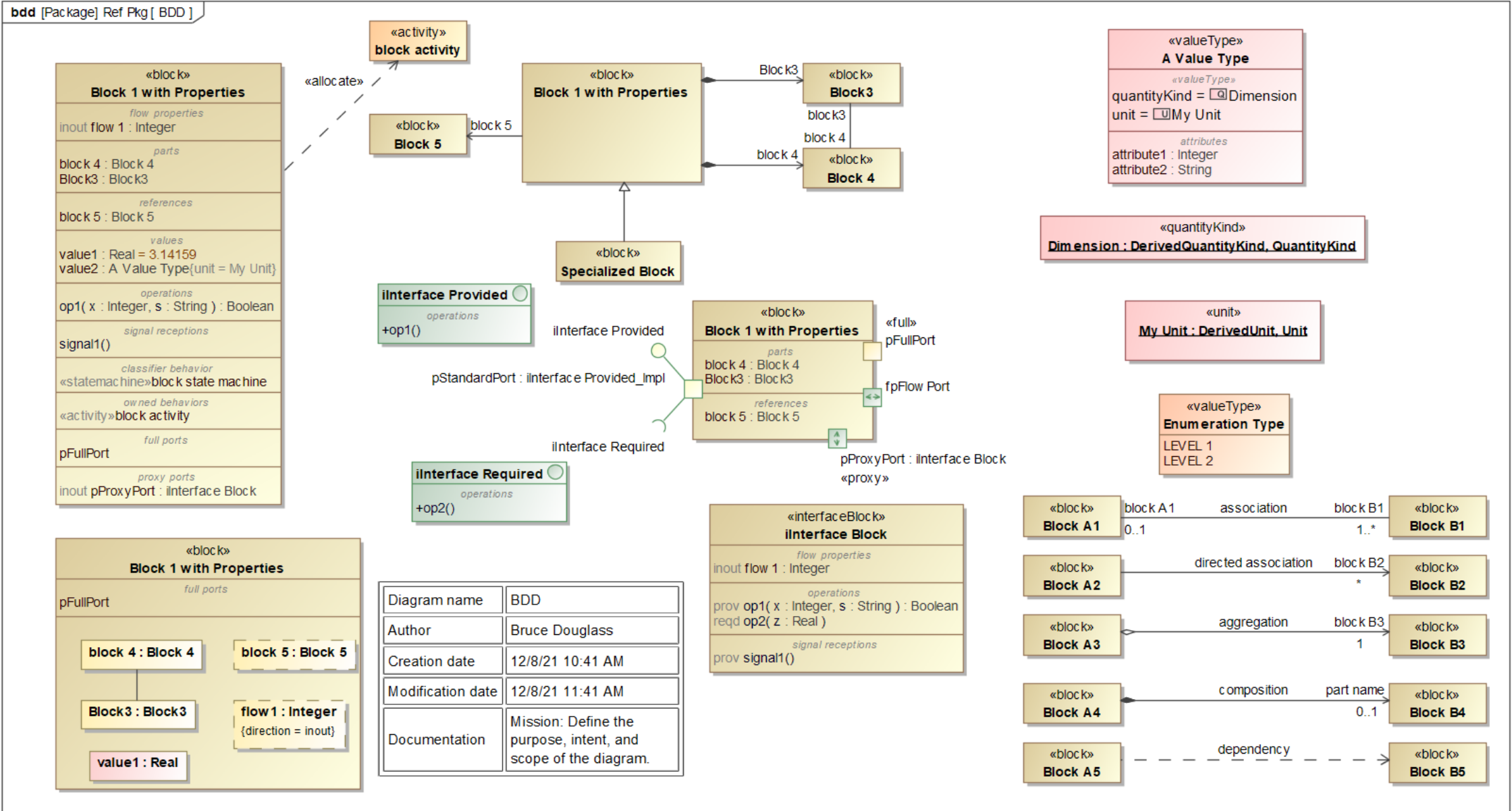
# SysML Quick Reference



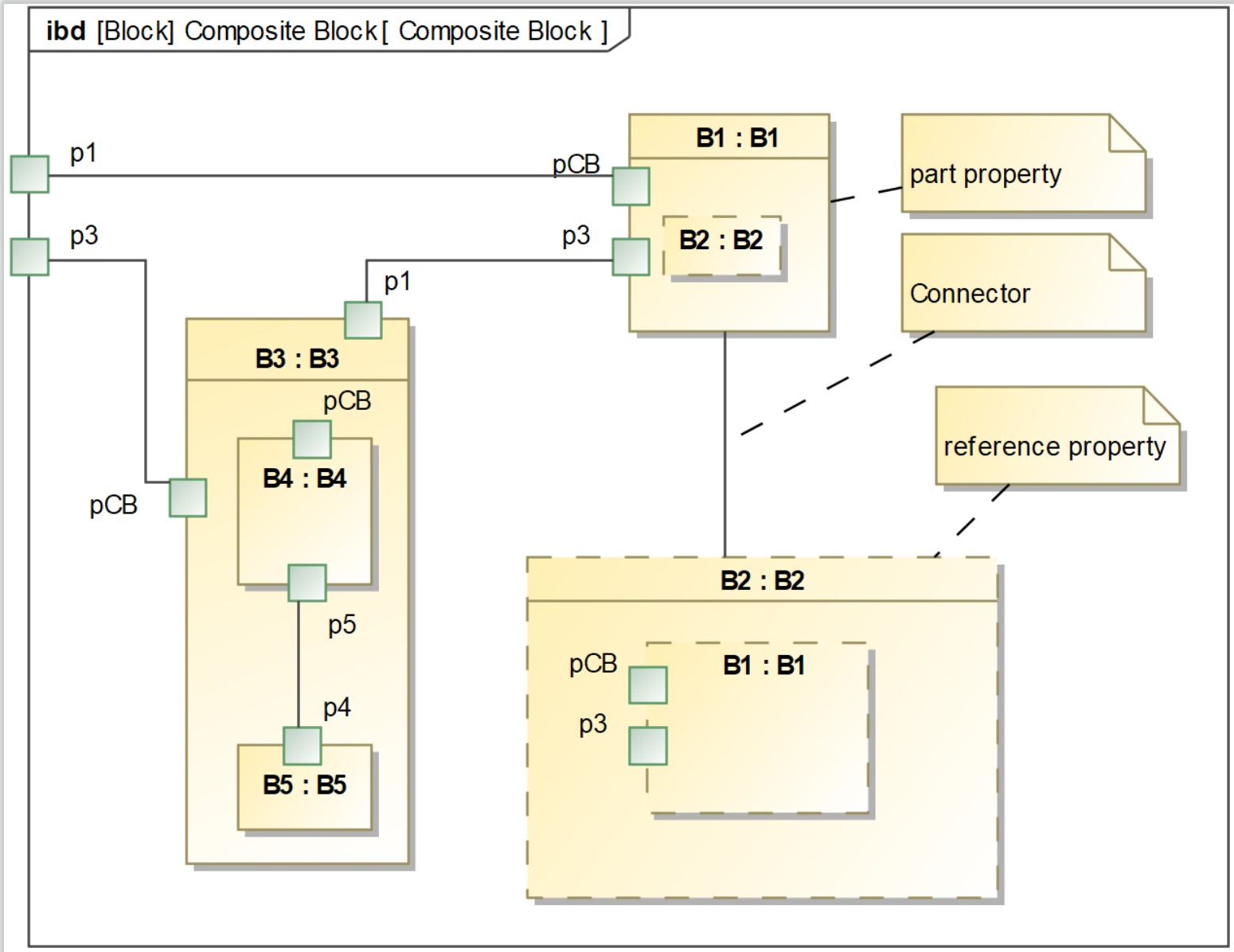
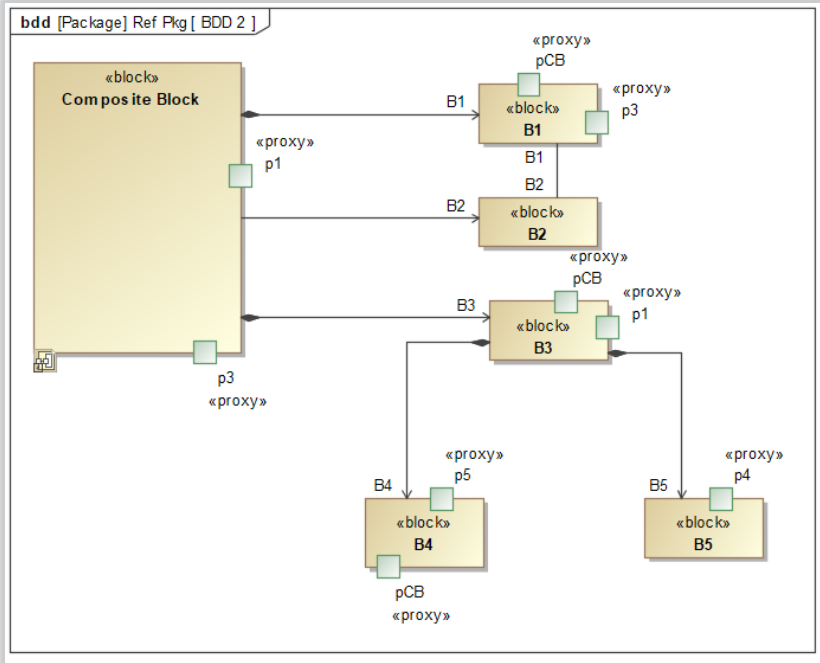
# Requirements (req) and Use Case (uc) Diagram



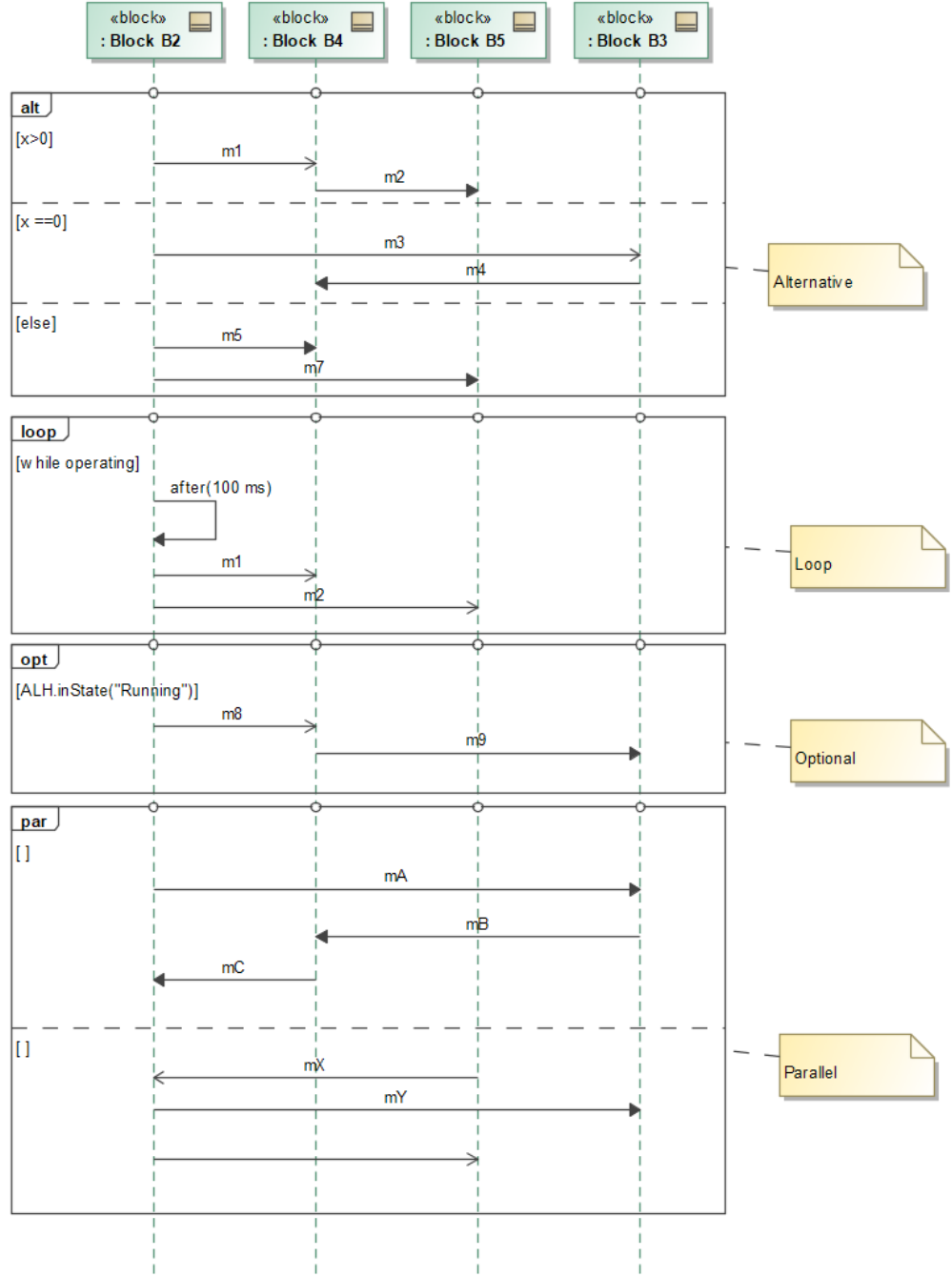
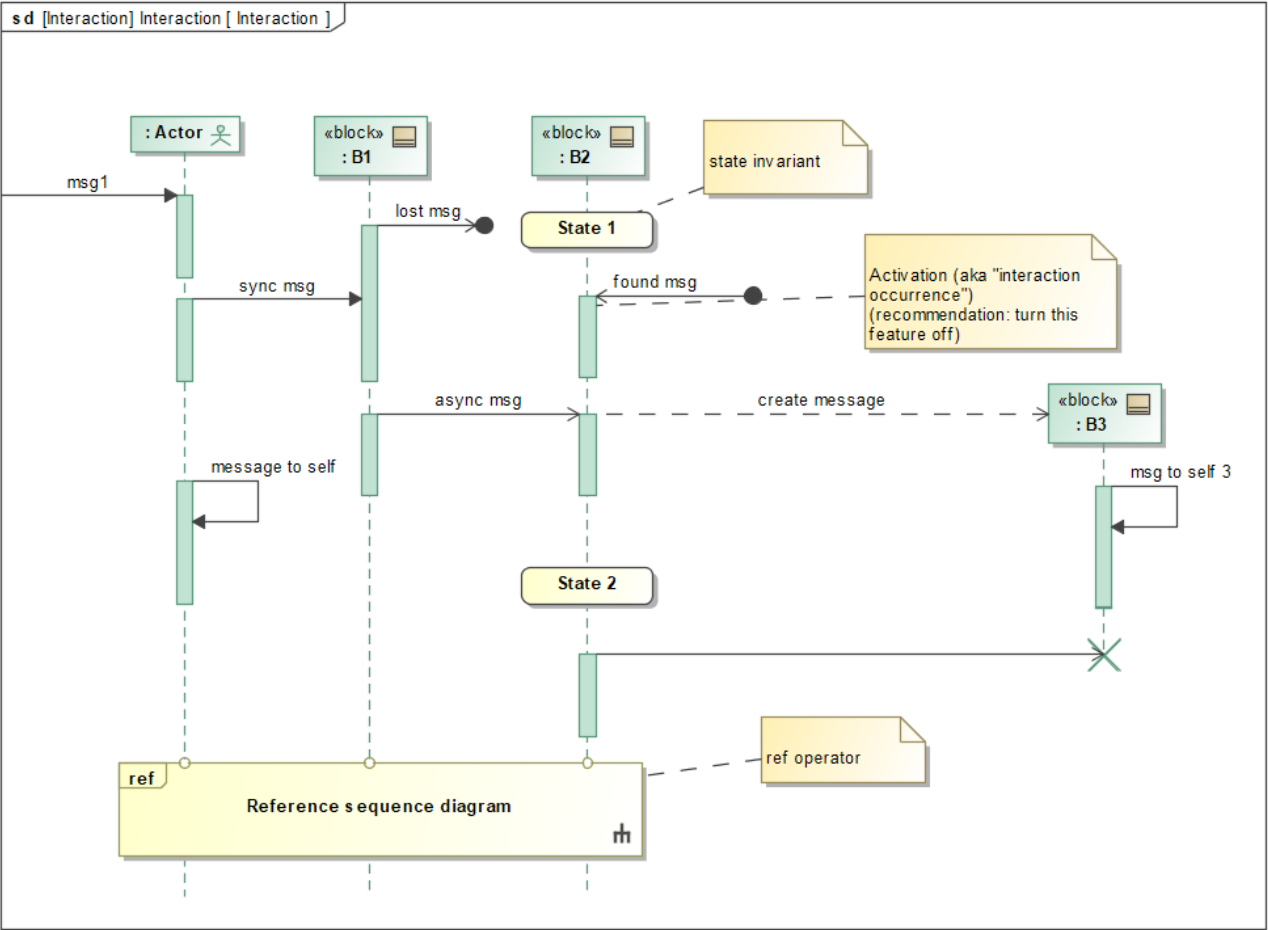
# Block Definition Diagram (bdd)



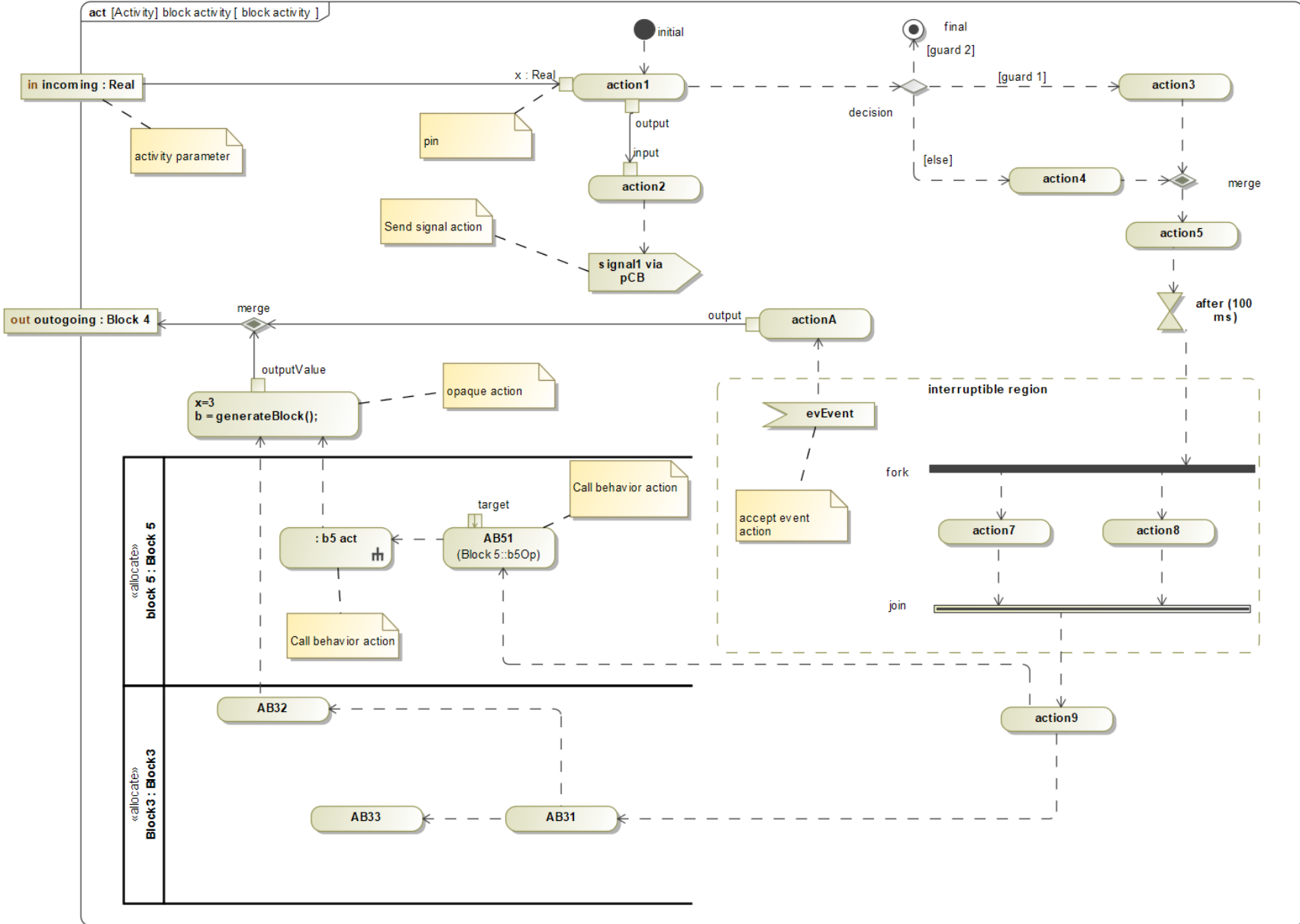
# Internal Block Diagram (ibd)



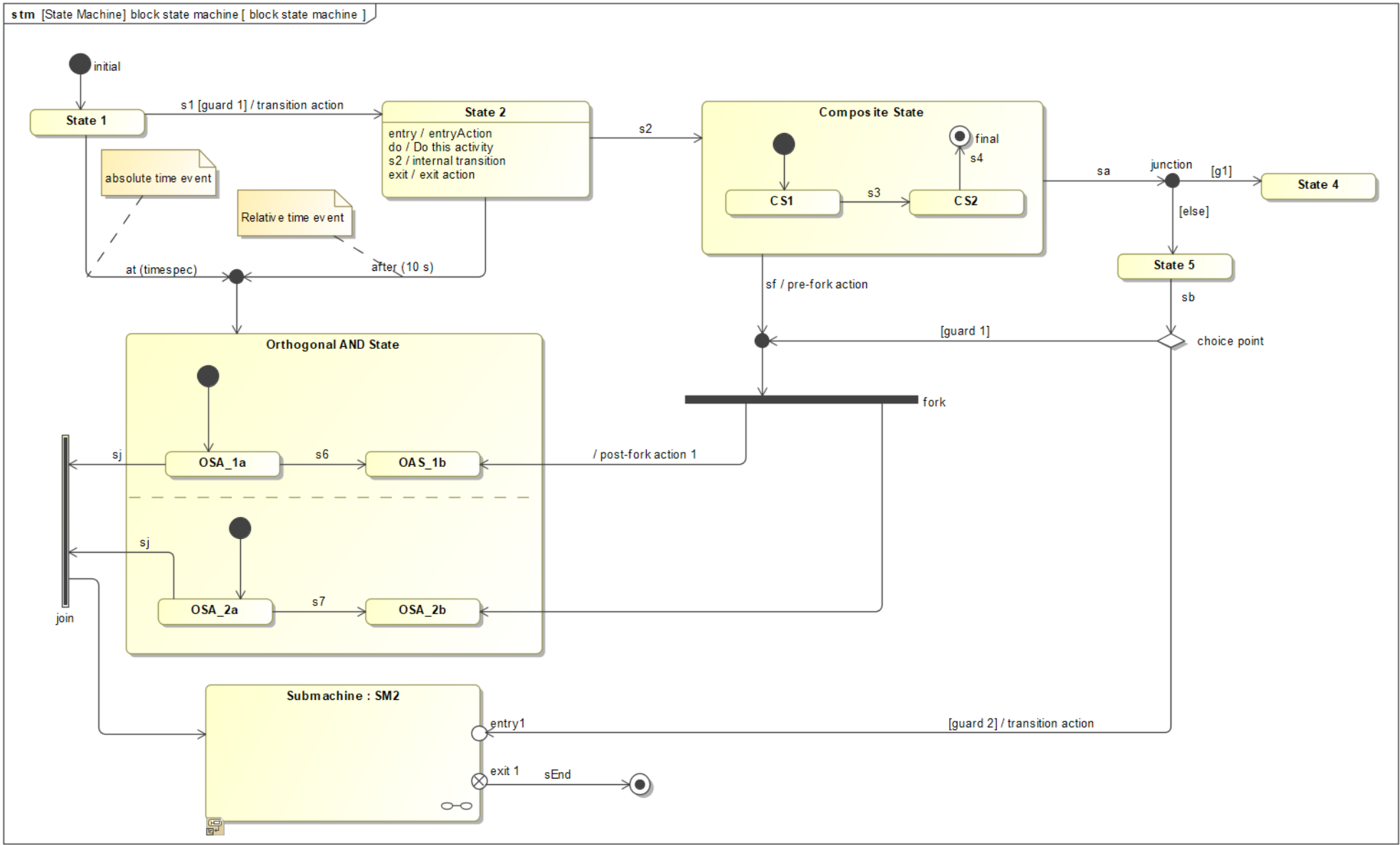
# Sequence Diagram (seq)



# Activity Diagram (act)



# State Machine Diagram (stm)



# Parametric (par) and Package (pkg) Diagram

